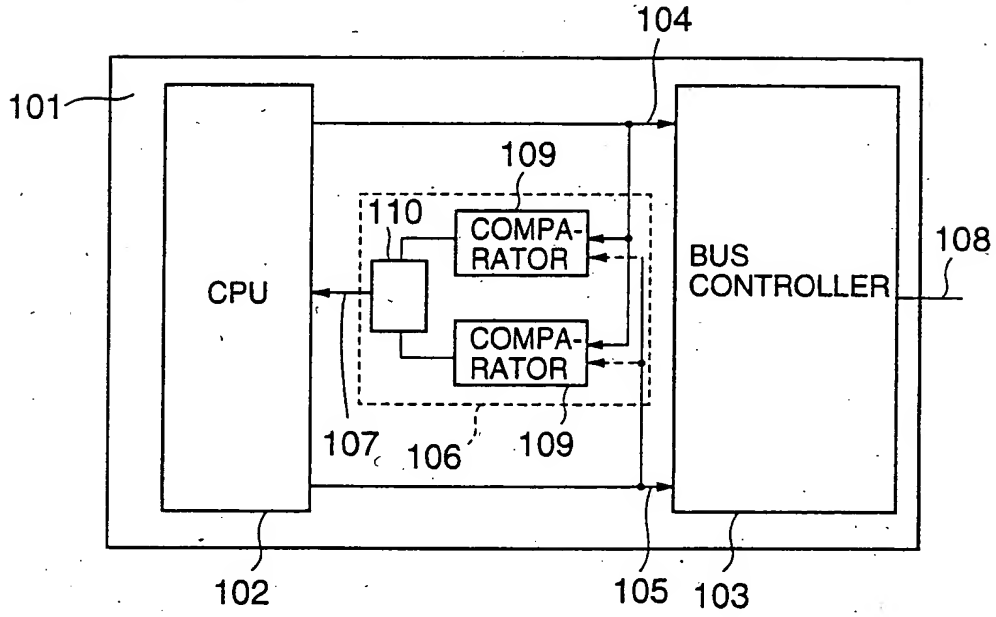
TITLE: MICROPROCESSOR
INVENTORS: Koutarou TAGAWA
SERIAL NO.: Unassigned
DOCKET NO.: 1614.1043D

FIG.1 PRIOR ART



TITLE: MICROPROCESSOR
INVENTORS: Koutarou TAGAWA
SERIAL NO.: Unassigned
DOCKET NO.: 1614.1043D

FIG.2

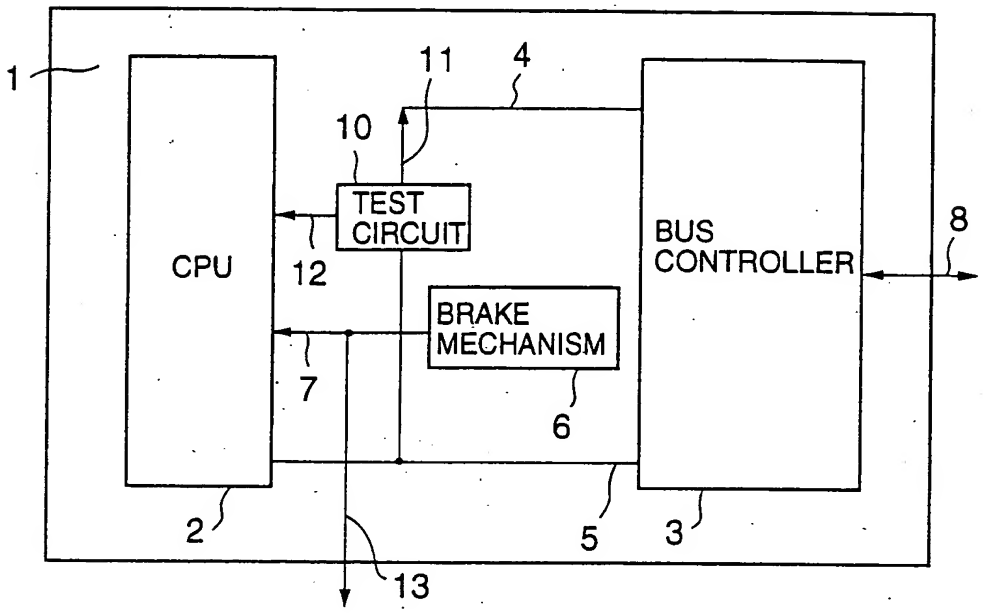
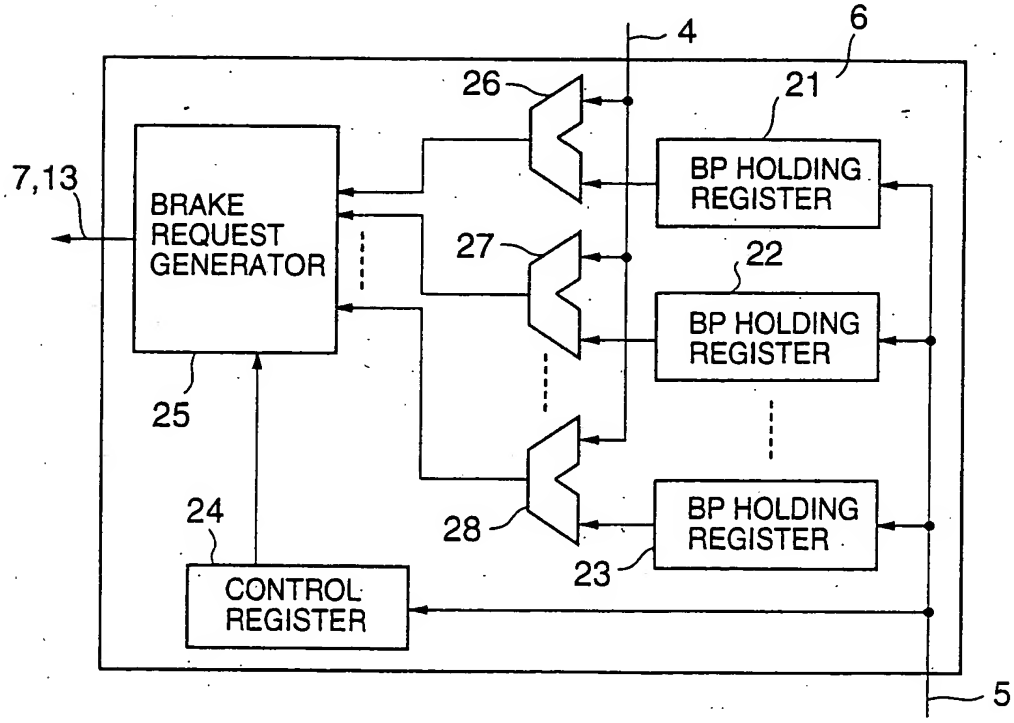


FIG.3



TITLE: MICROPROCESSOR
INVENTORS: Koutarou TAGAWA
SERIAL NO.: Unassigned
DOCKET NO.: 1614.1043D

FIG.4

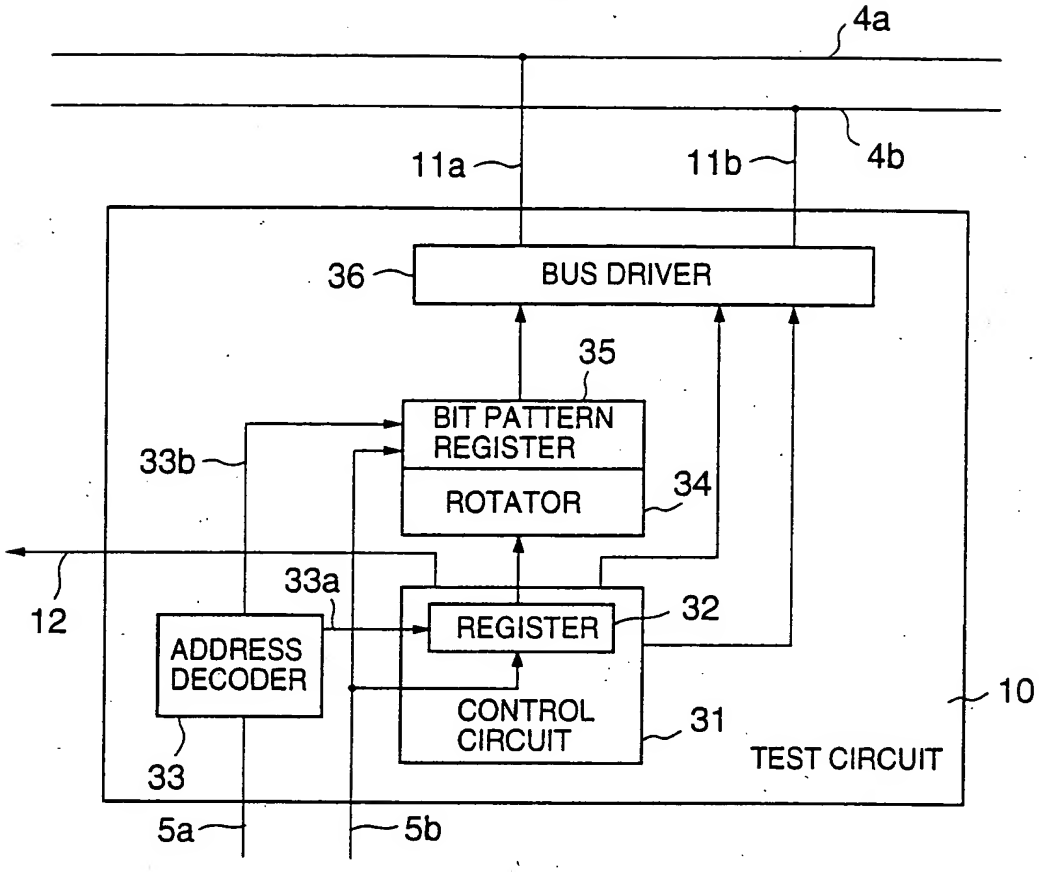


FIG.5

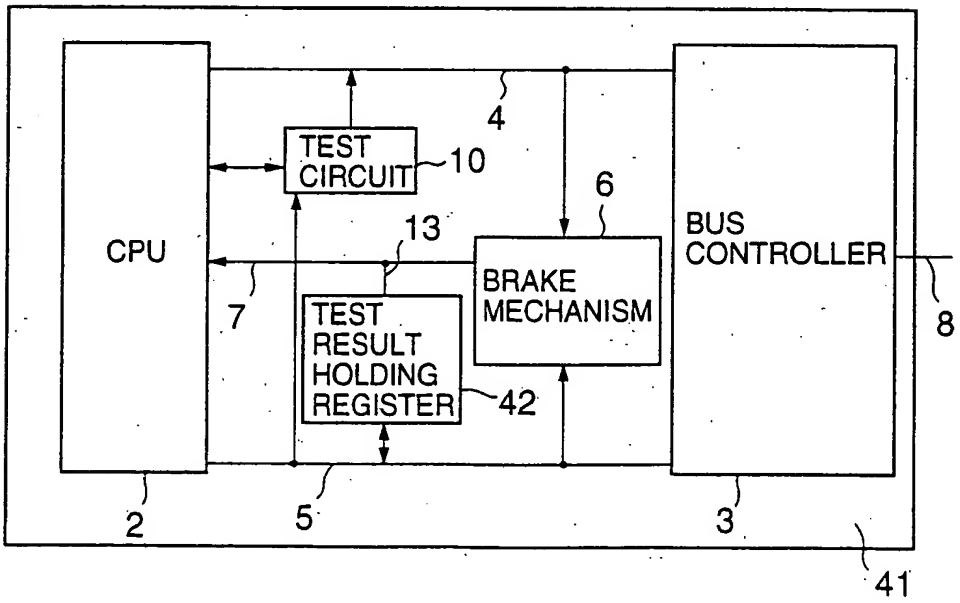


FIG.6

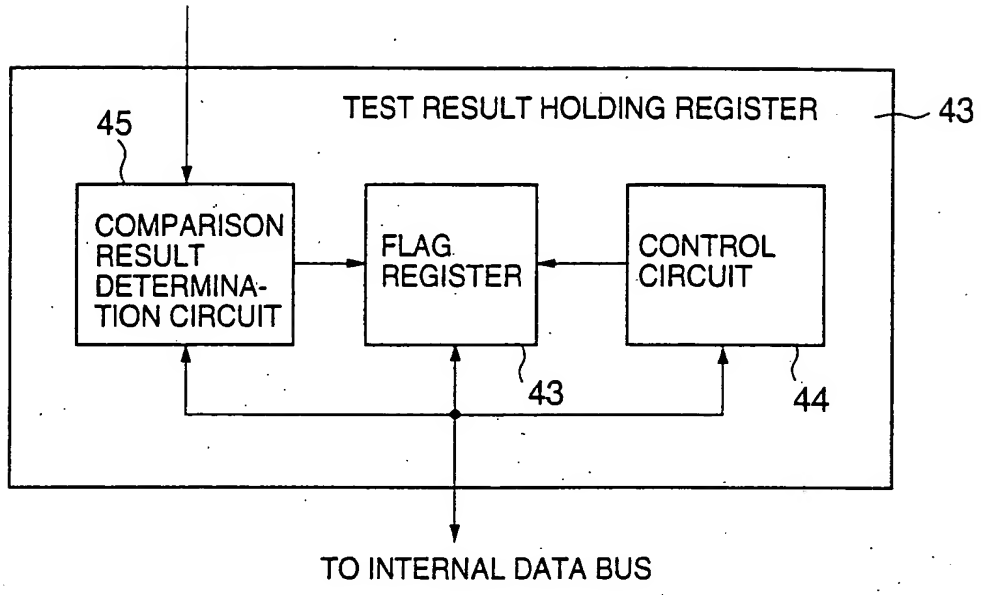

TEST RESULT HOLDING REGISTER
43
45
COMPARISON RESULT DETERMINA-TION CIRCUIT
FLAG REGISTER
CONTROL CIRCUIT
43
44
TO INTERNAL DATA BUS

FIG.7

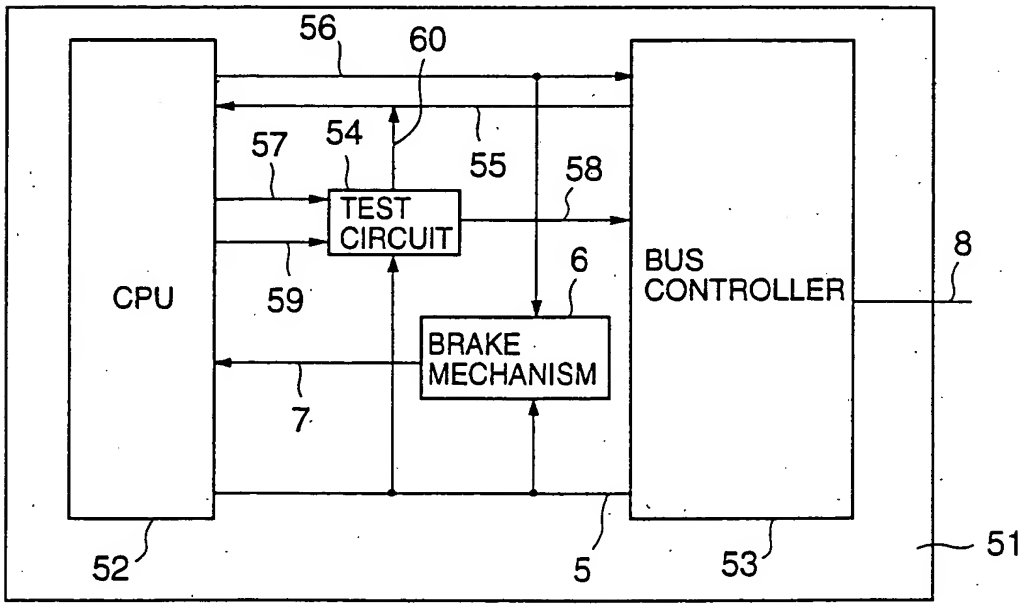

56
60
57
54
55
58
TEST CIRCUIT
59
6
CPU
BUS CONTROLLER
8
BRAKE MECHANISM
7
52
5
53
51

FIG.8

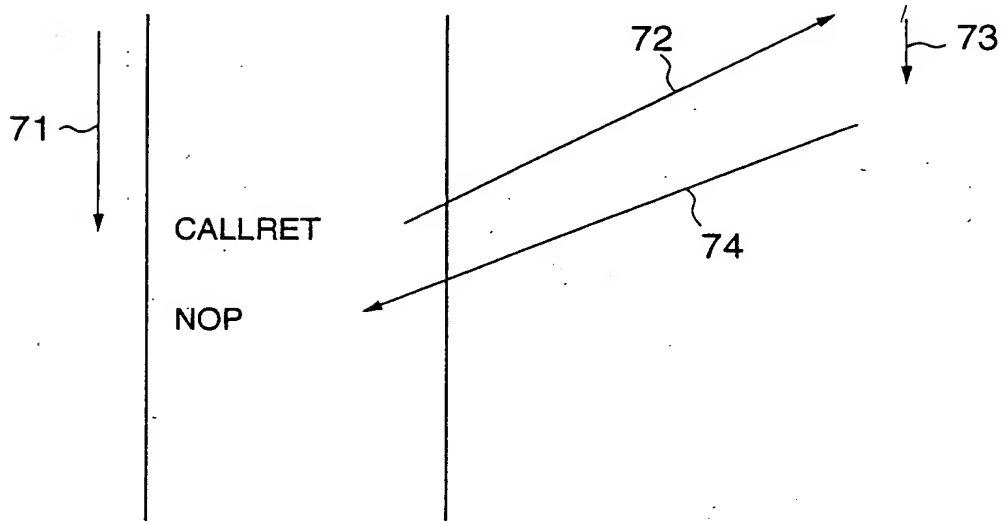

71
CALLRET
NOP
72
73
74

FIG.9

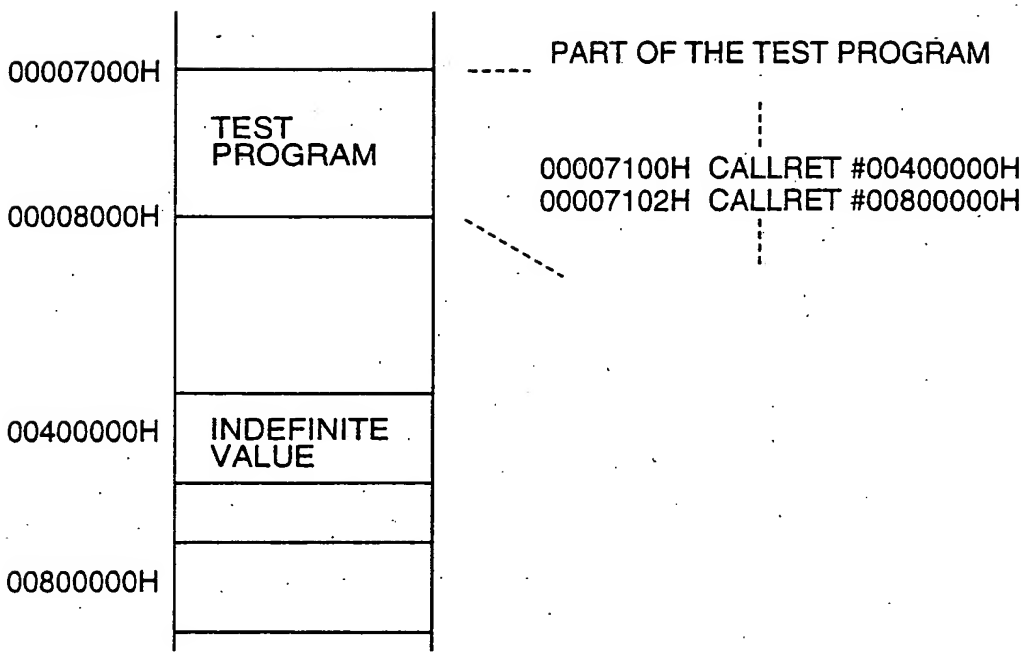

00007000H
TEST PROGRAM
00008000H
00400000H
INDEFINITE VALUE
00800000H
PART OF THE TEST PROGRAM
00007100H CALLRET #00400000H
00007102H CALLRET #00800000H

FIG.10

100	INSTRUCTION#1
101	INSTRUCTION#2
102	STIA
103	INSTRUCTION#3
104	INSTRUCTION#4
105	INSTRUCTION#5

FIG.11

						T0 ↓			
IF	#1	#2	STIA	#3	#4	#200	#5		
ID		#1	#2	STIA	#3	#4	#200	#5	
EX			#1	#2	STIA	#3	#4	#200	#5
MA				#1	#2	STIA	#3	#4	NOP

FIG.12

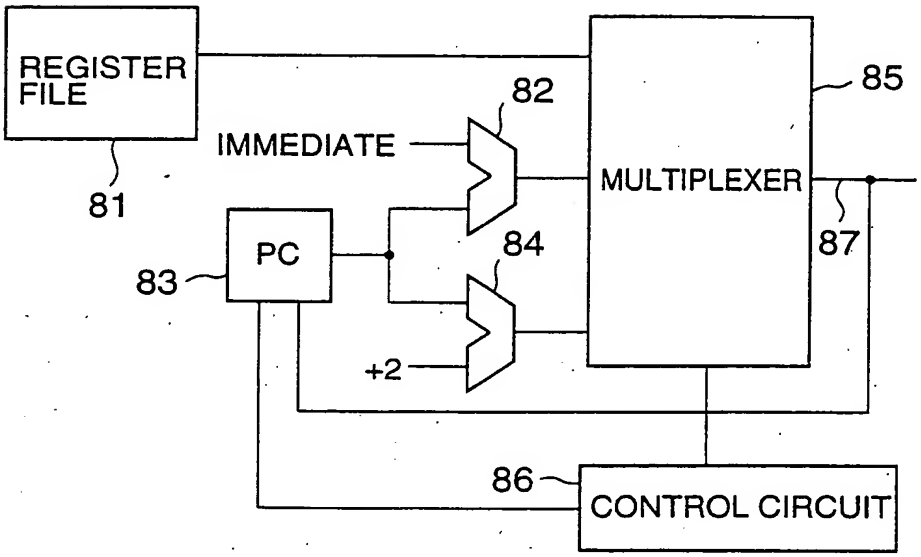


FIG.13

						T0 ↓	T1 ↓	T2 ↓	T3 ↓		
IF	#1	#2	STIA	#3	#4	#200	#400	#800	#1600	#5	#6
ID		#1	#2	STIA	#3	#4	#4	#4	#4	NOP	#5
EX			#1	#2	STIA	#3	#3	#3	#3	#4	NOP
MA				#1	#2	STIA	STIA	STIA	STIA	#3	#4

FIG.14

R0	00001000
R1	00002000
R2	00004000
R3	00008000
R4	00010000
⋮	⋮

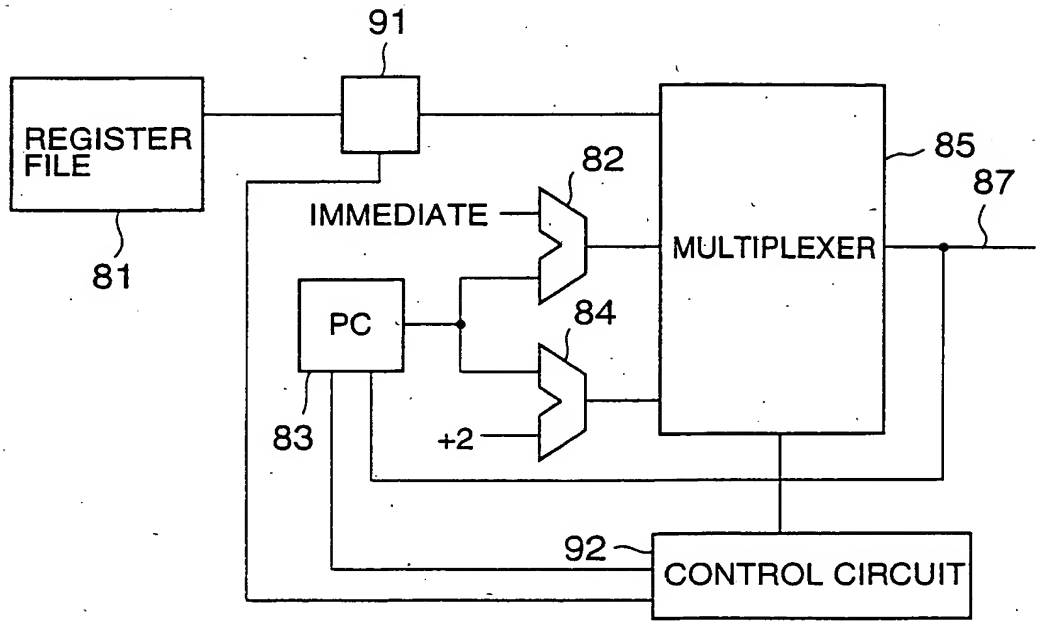



FIG.15